

TITLE OF THE INVENTION

IMAGE-CAPTURING APPARATUS, METHOD OF CONTROLLING  
THE SAME APPARATUS, AND COMPUTER PROGRAM PRODUCT  
5 FOR PROVIDING THE SAME METHOD

BACKGROUND OF THE INVENTION

Field of the Invention

10 The present invention relates to an image-capturing  
apparatus, such as an electronic still camera, for  
converting an optical image into an electrical image signal  
by a photoelectric conversion device and for recording the  
electrical image signal on a recording medium, and relates  
15 to a method of controlling the image-capturing apparatus,  
and to a computer program product for providing the method  
of controlling the image-capturing apparatus.

Description of the Related Art

20 A <sup>SOME</sup> ~~In those~~ models of conventional image-capturing  
apparatuses, such as electronic still cameras, ~~that~~ are  
capable of performing burst photo-taking (a photographic  
image is temporarily stored in a buffer memory, etc., time-  
consuming image processing and image compression are  
performed when there is time, and the photographed image is  
25 recorded (stored) on a recording medium, thus increasing the

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A frame speed of continuous photo-taking), <sup>of those,</sup> models which do not  
display the number of burst frames or which display only the  
number of burst frames on an external display have been  
commercially available, but there are no models which  
5 produce a display which clearly shows the number of  
photographs which can be taken and the number of frames of  
burst photo-taking or which produce the display in which a  
burst state is shared with another display. Also, there are  
A no models which clearly display the time <sup>remaining until</sup> ~~when the next~~  
10 A photo-taking <sup>again</sup> becomes possible when the storage area of a  
buffer memory is filled up to its limit.

As a result, in the above-described conventional  
example, there are problems in that the number of  
photographs for which continuous photo-taking is possible  
15 and the state in which the number of remaining photographs  
which can be recorded on a recording medium cannot be  
recognized at the same time, it is difficult to know the  
time when the next photo-taking will be possible, and the  
burst photo-taking state cannot be displayed unless a  
20 dedicated display member is prepared.

#### SUMMARY OF THE INVENTION

In a first aspect, the present invention provides an  
apparatus, as well as a method of controlling this apparatus  
25 and a computer program product implementing such a method,

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image in an image storage device, reading the image stored in the image storage device, and recording the read image on a recording medium, wherein the image-stored state of the image storage device and photo-taking-related information  
5 different from the image-stored state of the image storage device are displayed selectively, whereby the display of the number of images which can be stored in the image storage device can be improved.

The above and further objects, aspects and novel  
10 features of the invention will become more fully apparent from the following detailed description when read in conjunction with the accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

15 Fig. 1 is a block diagram showing the construction of an image-capturing apparatus according to a first embodiment of the present invention;

Fig. 2 is a flowchart showing the flow of the operation of the image-capturing apparatus according to the first  
20 embodiment of the present invention;

Fig. 3 is a flowchart showing the flow of the operation of the image-capturing apparatus according to the first embodiment of the present invention;

Figs. 4A, 4B, and 4C are diagrams showing examples of  
25 displays of the image-capturing apparatus according to the

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Fig. 5 is a flowchart showing the flow of the operation of an image-capturing apparatus according to a second embodiment of the present invention;

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embodiment of the present invention;

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according to the fourth embodiment of the present invention.

## 20

drawings.

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A first embodiment of the present invention is described below with reference to Figs. 1 to 5.

In this embodiment, the number of remaining photographs which can be written into a removable memory and the number of remaining photographs which can be stored in a buffer memory for continuous burst photo-taking are clearly displayed at the same time.

Fig. 1 is a block diagram showing the construction of an electronic still camera (film image reproduction apparatus) which is an image-capturing apparatus according to this embodiment.

Referring to Fig. 1, reference numeral 1 denotes a photographic lens unit, and reference numeral 2 denotes an AF (autofocus) driving circuit, which is composed of, for example, a stepping motor, for focusing an image on an image-capturing device 6 by adjusting the focal position of the lens inside the photographic lens unit 1 under the control of a microcomputer 16. Also, reference numeral 3 denotes a zoom driving circuit 3, which is composed of, for example, a stepping motor, for adjusting the focal distance of the photographic lens unit 1 by adjusting a variable-power lens position inside the photographic lens unit 1 under the control of a microcomputer 16. Reference numeral 4 denotes an iris driving circuit, which is composed of, for example, an automatic iris and the like, for adjusting the

Reference numeral 6 denotes an image-capturing device, which is composed of, for example, a CCD (charge-coupled

which is composed of, for example, a CCD (charge-coupled device) and the like, for photoelectrically converting an object image formed into an image on the image-capturing device 6 by the photographic lens unit 1 in order to pick up electrical signals. Reference numeral 7 denotes a clamping/CDS (correlation double sampling) circuit for performing a basic analog process before A/D conversion is performed and also for changing the clamping level.

Reference numeral 8 denotes an AGC (automatic gain control) apparatus for performing a basic analog process before A/D conversion is performed and also for changing the AGC reference level. Reference numeral 9 denotes an A/D converter for converting an analog output signal of the CCD 6 into a digital signal.

Reference numeral 10 denotes a video signal processing circuit for performing a filtering process, a color conversion process, and a gamma correction process on the digitized image data of the CCD 6 and outputs the resulting data to a memory controller 13. Also, this video signal processing circuit 10 has contained therein a D/A converter which converts a video signal input from the CCD 6 and image data which is input in reverse from the memory controller 13

into an analog signal and which outputs it to an EVF (electronic viewfinder) monitor 12 through an EVF driving circuit 11.

Switching of these functions is performed by the exchange of data with the microcomputer 16, and it is possible to output exposure information, focusing information, white balance, and autofocus information of the output signal of the CCD 6 to the microcomputer 16 as necessary. Based on the information, the microcomputer 16 performs white balance adjustment and gain adjustment. Also, the microcomputer 16 evaluates the autofocus information and communicates it to the AF driving circuit 2 so that the focusing lens inside the photographic lens unit 1 is driven. It is also possible to store image data in a buffer memory 19 through the memory controller 13 without performing any action in accordance with the instructions of the microcomputer 16. The video signal processing circuit 10 also has a function for a compression process, such as JPEG (Joint Photographic Experts Group).

Also, in the case of continuous photo-taking, image data is stored once in the buffer memory 19, and image processing and compression processing by the video signal processing circuit 10, which take time, are performed by reading image data, which is not yet processed, stored in the buffer memory 19 through the memory controller 13,

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In the memory controller 13, the digital image data, which is not yet processed, input from the video signal processing circuit 10 is stored in the buffer memory 19, a processed digital image is stored in a memory 14, or conversely, image data is output to the video signal processing circuit 10 from the buffer memory 19 and the memory 14. Also, it is possible for the memory controller 13 to store a video image sent from an external interface 15 in the memory 14 and to output an image stored in the memory 14 from the external interface 15. There are cases in which the memory 14 can be freely mounted to and removed from the main unit of the display device.

Reference numeral 17 denotes a power-supply circuit which supplies power required for each IC (integrated circuit) and the driving system. Reference numeral 18 denotes an operation member, the operating state thereof being transmitted to the microcomputer 16, and this microcomputer 16 controls each section according to the operating state of the operation member 18. Reference numeral 20 denotes a release button, which is an input switch of the operation member 18, having movable contacts

20a and 20b. When the release button 20 is half-depressed, only one of the movable contacts (contact 20a) is turned on (closed), and when the release button 20 is fully depressed, the contact 20b is turned on, thereby holding and recording a photographic image.

Reference numeral 21 denotes a liquid-crystal display driving circuit which drives an external liquid-crystal display device 22 and an intra-finder liquid-crystal display device 23 in accordance with the display content instructions of the microcomputer 16. Also, in the intra-finder liquid-crystal display device 23, a backlight, such as an LED (light-emitting diode) (not shown), is disposed, and the LED is also driven by the liquid-crystal display driving circuit 21. Also, it is possible for the liquid-crystal display driving circuit 21 to cause specific segments to blink in accordance with instructions from the microcomputer 16. The liquid-crystal display driving circuit 21 also has a function of changing the display color.

A means for converting an optical image into an electric image signal by a photoelectric conversion means comprises the CCD 6, the clamping/CDS circuit 7, the AGC 8, and the A/D converter 9. A storage means for temporarily storing an image comprises the buffer memory 19. A control means for controlling <sup>temporary storage of</sup> ~~so that an image is temporarily stored~~ in the buffer memory 19 comprises the video signal

processing circuit 10 and the memory controller 13. A removable recording medium comprises the memory 14. A control means for performing control so that an electric image signal is read from the buffer memory 19 and is  
5 subjected to image processing comprises the video signal processing circuit 10 and the memory controller 13. A control means for performing control so that an electric image signal is stored in a removable recording medium comprises the memory controller 13. Also, a display means  
10 for displaying a photo-taking state, such as the number of photographs which can be taken, comprises the liquid-crystal display driving circuit 21 and the intra-finder liquid-crystal display device 23.

Next, a description is given of an operation in the photo-taking mode of an image-capturing apparatus according to this embodiment with reference to the flowchart in Fig. 2.

Initially, in step S201, it is determined whether or not a power-off timer inside the microcomputer 16 has reached the set time limit. When it is determined that time  
20 has run out, the process proceeds to step S215, whereby a power-off process is performed. When it is determined that time has not yet run out, the process proceeds to step S202 (when the photo-taking mode starts, the power-off timer has not yet reached the set time limit).

In step S202, it is determined whether or not one of

the movable contacts (contact 20a) of the release button 20 is turned on. When it is determined that it is on, the process proceeds to step S203, whereby a process for updating the time setting in the power-off timer is performed. When, in contrast, it is not on, the process proceeds to step S204.

In step S203, after the process for updating the power-off timer is performed, the process proceeds to step S204. In step S204, the image from the CCD 6 is input by the clamping/CDS circuit 7, the AGC 8, and the A/D converter 9.

Next, the process proceeds to step S205, whereby based on the exposure conditions and the white balance information, which are outputs of the video signal processing circuit 10, control is performed so that an appropriate brightness and an appropriate color are obtained by computing and determining a gain value in the AGC 8, an f-stop number to be sent to the iris driving circuit 4, an electronic shutter value for control of the CCD 6, and R and B gains used in the video signal processing circuit 10.

Thereafter, the process proceeds to step S206, whereby the amount of lens driving is computed and determined on the basis of focusing information, which is an output of the video signal processing circuit 10, and some of the lenses of the photographic lens unit 1 are driven by using the AF driving circuit 2, thereby performing focusing adjustment.

Thereafter, the process proceeds to step S207, whereby the image signal controlled in step S205 is sent to the EVF driving circuit 11, and the image signal is displayed on the EVF monitor 12.

5        Thereafter, the process proceeds to step S208, whereby the photo-taking information determined in steps S205 and S206 is sent to the liquid-crystal display driving circuit 21, and the photo-taking information is displayed on the external liquid-crystal display device 22 and the intra-  
10    finder liquid-crystal display device 23. Also, the backlight of the intra-finder liquid-crystal display device 23 is switched on.

Thereafter, the process proceeds to step S209, whereby it is determined whether or not the contact 20b of the  
15    release button 20 is turned on. When it is determined that it is on, the process proceeds to step S210, whereby the image is stored. When, in contrast, it is determined that it is not on, the process returns to step S201. In step  
20    S210, the image signal is sent to the memory controller 13, and the image is temporarily stored in the buffer memory 19.

Thereafter, the process proceeds to step S211, whereby an operation is started to process an image stored in the buffer memory 19, which is not yet processed, when the load of the video signal processing circuit 10 is at a level at  
25    which image processing can be performed, to compress the

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5        Thereafter, the process proceeds to step S212, whereby  
it is determined whether or not the photo-taking of the next  
frame is possible. Here, if there is a free storage area of  
the buffer memory 19, photo-taking of the next frame is  
possible, and in that case, the process proceeds to step  
10 S213. If there is no free storage area of the buffer memory  
19, photo-taking of the next frame is impossible, and the  
process waits for image processing, a compression process,  
and a storing-in-memory process for one frame to terminate.

In step S214, the power-off timer setting is cleared,  
20 and the power-off timer is set to a state in which time has  
run out, after which the process returns to step S201. When,  
in contrast, it is determined in step S201 that the power-  
off timer has reached a state in which time has run out, in  
step S215, the display of the EVF monitor 12 is turned off  
25 by the EVF driving circuit 11, and the backlight of the

intra-finder liquid-crystal display device 23 is also turned off.

Thereafter, the process proceeds to step S216, whereby all of the image processing, compression, and storing in the memory, which are started in step S211, are terminated, and the process waits for the storage area of the buffer memory 19 to become free.

Thereafter, the process proceeds to step S217, whereby an instruction is issued to the power-supply section 17 so that unnecessary power supplies are switched off, and then this processing operation is terminated.

Next, the features of the image-capturing apparatus according to this embodiment are described with reference to Fig. 3 and Figs. 4A, 4B, and 4C.

Fig. 3 is a flowchart showing the details of a process for display on the external liquid-crystal display device 22 and the intra-finder liquid-crystal display device 23, which is performed in step S208 in Fig. 2. Figs. 4A, 4B, and 4C are diagrams showing examples of displays thereof.

In Fig. 3, initially, in step S301, the number of photographs for which continuous burst photo-taking is possible, which can be obtained from the free area of the buffer memory 19, is compared with the number of remaining photographs which can be written into the removable memory 14. When it is determined that the number of photographs

5    photographs for which continuous burst photo-taking is possible is less than the number of remaining photographs which can be written into the memory 14, the process proceeds to step S303.

10 the upper limit of the display of photographs for which  
continuous burst photo-taking is possible being the number  
of remaining photographs. Also, in step S303, the number of  
photographs for which continuous burst photo-taking is  
possible, which can be obtained from the free area of the  
15 buffer memory 19, is placed in a blinking display state.

A <sup>Either when</sup> ~~When~~ the above-described step S302 is terminated or when the  
A above-described step S303 is terminated, ~~in either case,~~  
this processing operation is terminated.

Figs. 4A, 4B, and 4C are diagrams showing examples of  
20 displays in the above-described display process, in which  
the maximum number of frames for which continuous burst  
photo-taking is possible is 12.

In Figs. 4A, 4B, and 4C, reference numeral 100 denotes a focus display section, reference numeral 101 denotes a shutter speed (in seconds) display section composed of four



digits, each composed of seven segments, reference numeral 102 denotes an f-stop number display section composed of two digits, each composed of seven segments, and reference numeral 103 denotes a continuous-burst-photo-taking-capable film counter display section of two digits.

Fig. 4A shows a case in which the number of frames for which continuous burst photo-taking is possible is eight, four frames in the buffer memory 19 are not yet processed, and the number of frames which can be recorded into the memory 14 is 13 or more. In this case, the counter display section 103 shows a switched-on display state.

Fig. 4B shows a case in which the number of frames for which continuous burst photo-taking is possible is 12, the storage area of the buffer memory 19 is free, and the number of frames which can be recorded into the memory 14 is 12. In this case, the counter display section 103 has changed from the switched-on display state to a blinking display state.

Fig. 4C shows a case in which the number of frames for which continuous burst photo-taking is possible is from 8 to 12, and the number of frames which can be recorded into the memory 14 is 8. In this case, the counter display section 103 has changed from the switched-on display state to a blinking display state.

As described above, according to this embodiment, it is

possible to grasp the number of photographs for which continuous burst photo-taking is possible, while at the same time grasping the number of remaining frames of a removable recording medium. The situation in which the recording area of the recording medium is filled up to its limit and the shutter release opportunity is missed can be avoided, and ease of operation is improved. Also, the relationship between two counters for the number of photographs which can be taken and the number of photographs for which continuous burst photo-taking is possible is not present in cameras using silver halide roll film, and a clear display of these matters is a technology required most strongly in electronic still cameras in the future.

In the image-capturing apparatus according to this embodiment, the functions of the above-described embodiment are realized by a computer by reading and executing a control program stored in a recording medium. However, the present invention is not limited to this example, and it is a matter of course that a case in which an OS (operating system) operating on a computer performs a part or the entirety of actual processing according to the instructions of the control program, and the functions of the above-described embodiment are realized by the processing, is included within the scope of the present invention.

As a computer program product, such as a storage medium

for storing control programs, for example, a floppy disk, a hard disk, an optical disk, a magneto-optical disk, a CD-ROM (Compact Disk-Read Only Memory), a CD-R (Compact Disk Recordable), a magnetic tape, a non-volatile memory card, a ROM chip, etc., can be used.

(Second Embodiment)

A second embodiment of the present invention is described below with reference to Fig. 5.

Since the basic construction of an image-capturing apparatus according to this embodiment is the same as that of the first embodiment in Fig. 1, the description also refers to Fig. 1. Although in the above-described first embodiment, a case is shown as an example in which the display section changes from a switched-on display state to a blinking display state, in this embodiment, a display is produced in which the color of the display section is changed.

Fig. 5 is a flowchart showing the details of a display process showing the features of an image-capturing apparatus according to this embodiment, which corresponds to Fig. 3 in the above-described first embodiment.

In Fig. 5, initially, in step S501, the number of photographs for which continuous burst photo-taking is possible, which is obtained from the free area of the buffer

memory 19, is compared with the number of remaining  
photographs which can be written into the removable memory  
14. When it is determined that the number of photographs  
for which continuous burst photo-taking is possible is equal  
5 to or greater than the number of remaining photographs which  
can be written into the memory 14, the process proceeds to  
step S502. When it is determined that the number of  
photographs for which continuous burst photo-taking is  
possible is less than the number of remaining photographs  
10 which can be written into the memory 14, the process  
proceeds to step S503.

In step S502, the number of photographs for which  
continuous burst photo-taking is possible is displayed by  
changing the color (for example, red) of the counter display  
15 section with the upper limit of the display of the number of  
photographs for which continuous burst photo-taking is  
possible being the number of remaining photographs. In step  
S503, the number of photographs for which continuous burst  
photo-taking is possible, which is obtained from the free  
20 area of the buffer memory 19; is displayed by an initial  
color (for example, green).

*A* <sup>Either when</sup> ~~When~~ the above-described step S502 is terminated or  
*A* when the above-described step S503 is terminated, ~~in either~~  
*A* ~~case~~, this processing operation is terminated.

25 Since the remaining construction and operation/working-

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effects in this embodiment are the same as those of the above-described first embodiment, descriptions thereof are omitted.

5 (Third Embodiment)

A third embodiment of the present invention is described below with reference to Fig. 6 and Figs. 7A, 7B, and 7C.

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10 Since the basic construction of an image-capturing apparatus according to this embodiment is the same as that of the first embodiment in Fig. 1, the description also refers to Fig. 1. Although in the above-described first and second embodiments, a case is shown as an example in which the number of remaining photographs which can be written  
15 into the removable memory 14 and the number of photographs for which continuous burst photo-taking is possible, which is obtained from the free area of the buffer memory 19 for continuous burst photo-taking, are clearly displayed at the same time, in this embodiment, a timing at which the next  
20 photo-taking becomes possible is displayed as a display of the number of burst frames.

Fig. 6 is a flowchart showing the details of a display process showing the features of an image-capturing apparatus according to this embodiment, which corresponds to Figs. 3  
25 and 5 in the above-described first and second embodiments.

Figs. 7A, 7B, and 7C are diagrams showing examples of displays of the image-capturing apparatus according to this embodiment. In Figs. 7A, 7B, and 7C, reference numeral 700 denotes a focus display section, reference numeral 701 denotes a shutter speed (in seconds) display section composed of four digits, each composed of seven segments, reference numeral 702 denotes an f-stop number display section composed of two digits, each composed of seven segments, and reference numeral 703 denotes a continuous-burst-photo-taking-capable film counter display section of two digits.

In Fig. 6, initially, in step S601, it is determined whether or not the storage area of the buffer memory 19 is filled up to its limit and a state is reached in which continuous burst photo-taking is impossible. When it is determined that a state is reached in which continuous burst photo-taking is impossible, the process proceeds to step S603, and when it is determined that a state is reached in which continuous burst photo-taking is possible, the process proceeds to step S602.

In step S602, as shown in Fig. 7A, the number of photographs for which continuous burst photo-taking is possible is displayed on the continuous-burst-photo-taking-capable film counter display section 703. Here, it is shown that the number of photographs for which continuous burst

photo-taking is possible is eight.

In step S603, as shown in Fig. 7B, "0" is displayed on the continuous-burst-photo-taking-capable film counter display section 703, after which, as shown in Fig. 7C, the continuous-burst-photo-taking-capable film counter display section 703 is changed to a time-until-photo-taking-becomes-possible display section 704, so that a time until photo-taking becomes possible is displayed thereon. Here, it is shown that photo-taking will become possible in four seconds. The phrase "photo-taking becomes possible" here refers to a free area for one frame becoming available in the buffer memory 19. When the above-described step S602 is terminated or when the above-described step S603 is terminated, in either case, this processing operation is terminated.

In the manner as described above, according to this embodiment, since the time when the next photo-taking will become possible becomes clear, it is easy to anticipate a shutter release opportunity, and at the same time, since the state of the image-capturing apparatus is clear, handling of the image-capturing apparatus is considerably improved. Also, in comparison with cameras using silver halide roll film, in which the next frame can be photographed by merely feeding film and releasing a shutter, electronic still cameras have a long photographing waiting time due to image processing, image compression, transfer, etc. Unlike

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cameras using silver halide film in which termination of a mechanical operation can be known by sound, in electronic still cameras, the time when processing will be terminated cannot be estimated, and this is more of a problem when the number of pixels is large. Therefore, the technology relating to this embodiment, such as that described above, will become even more important in the future.

Since the remaining construction and operation/working-effects in this embodiment are the same as those of the above-described first embodiment, descriptions thereof are omitted.

(Fourth Embodiment)

A fourth embodiment of the present invention is described below with reference to Fig. 8 to Figs. 10A, 10B, and 10C.

Since the basic construction of an image-capturing apparatus according to this embodiment is the same as that of the first embodiment in Fig. 1, the description also refers to Fig. 1.

Fig. 8 is a flowchart showing the details of a display process showing the features of an image-capturing apparatus according to this embodiment, which corresponds to Figs. 3, 5, and 6 in the above-described first, second, and third embodiments. Figs. 9A and 9B, and Figs. 10A, 10B, 10C and



10D are diagrams showing examples of displays. In Figs. 9A and 9B, reference numeral 900 denotes a focus display section, reference numeral 901 denotes a shutter speed (in seconds) display section of four digits, each composed of seven segments, reference numeral 902 denotes an f-stop number display section composed of two digits, each composed of seven segments, and reference numeral 903 denotes a memory-recordable number-of-remaining-photographs display section of two digits. In Figs. 9 and 10, reference numeral 904 denotes a bar display section.

In Fig. 8, initially, in step S801, it is determined whether or not the time is within a predetermined period after photo-taking. When it is determined that the time is not within a predetermined period after photo-taking (i.e., before photo-taking), the process proceeds to step S803, and when it is determined that the time is within a predetermined period after photo-taking (i.e., after photo-taking), the process proceeds to step S802.

In step S803, as shown in Fig. 9A, the setting amount of exposure correction is displayed on the bar display section 904. Here, it is shown that "+1EV" (EV: exposure value) is set as the amount of exposure correction. Also, a state in which continuous burst photo-taking of eight frames is possible is displayed on the memory-recordable number-of-remaining-photographs display section 903.

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In step S802, as shown in Fig. 9B, the state of the buffer memory 19 is displayed on the bar display section 904 instead of the exposure correction display. Here, the maximum number of continuous burst photo-taking is nine frames, six frames out of nine frames being processed or in a state of waiting for processing, and a state in which continuous burst photo-taking of three frames is possible is displayed on the memory-recordable number-of-remaining-photographs display section 903.

- 10 A <sup>Either when</sup> ~~when~~ the above-described step S802 is terminated or  
A when the above-described step S803 is terminated, ~~in either~~  
A ~~case~~, this processing operation is terminated.

Figs. 10A, 10B, 10C, and 10D are diagrams showing examples of displays in a process, corresponding to step S215 in Fig. 2 in the above-described first embodiment, after photo-taking in this embodiment.

Since the time set in the power-off timer has elapsed during this processing, initially, the focus display section, the shutter speed (in seconds) display section, the f-stop number display section, and the memory-recordable number-of-remaining-photographs display section are switched off. The number of photographs which can be recorded in the buffer memory 19 for continuous burst photo-taking is shown as three in Fig. 10A, two in Fig. 10B, and one in Fig. 10C, and finally, it becomes zero in Fig. 10D, and the bar display

section 904 is switched off, and thus all the sections are switched off.

In the manner as described above, according to this embodiment, it is possible to display the state of

continuous burst photo-taking without preparing a dedicated display member, the photo-taking possible state of the image-capturing apparatus is made clear, and handling is improved. In comparison with cameras using silver halide

A film, in electronic still cameras, contents <sup>for</sup> which <sup>there is a demand</sup> are

10 A ~~desired~~ to be displayed, such as automatic white balance, degree of compression, preview, etc., have increased.

Accordingly, the sharing of the display section, such as

A that described above, <sup>will become</sup> ~~is~~ an important technology for cameras using silver halide film in the future.

15 Also, in a case where a display member is shared among a camera using silver halide film and an electronic still camera or a video camera, it is also possible to display information corresponding to the respective cameras. For example, it is possible to use the display member as a

20 display member for displaying a film state in the silver halide camera, for displaying the state of a buffer memory for continuous photo-taking in the electronic still camera, and for displaying the amount of remaining recording tape in the video camera.

25 In addition, in a case where the display section is

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used for a hybrid camera for recording on silver halide film and in semiconductor memory, in which a camera section is shared, it is also possible to selectively switch the display contents according to the selection of the recording medium and the switching of the photo-taking mode.

Since the remaining construction and operation/working-effects in this embodiment are the same as those of the above-described first embodiment, descriptions thereof are omitted.

The individual components shown in schematic or block form in the Drawings are all well known in the camera arts and their specific construction and operation are not critical to the operation or best mode for carrying out the invention.

For example, the software structure and the hardware construction of the above embodiments can be substituted appropriately.

In the present invention, each of the above-described embodiments or technological elements thereof may be combined as required. Also, the present invention as claimed, or the entirety or part of the construction of the embodiments may form one apparatus, may be coupled with another apparatus, or may be elements which constitute an apparatus.

In addition, the present invention can be applied to a

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A variety<sup>of</sup> types of cameras, such as video-movie cameras, video  
still cameras, single-lens reflex cameras, lens shutter  
cameras, or monitor cameras, to image-capturing apparatuses  
other than cameras, image reading apparatuses, optical  
5 apparatuses, and other apparatuses, can be applied to  
apparatuses which are used in these cameras, image-capturing  
apparatuses, image reading apparatuses, the optical  
apparatuses, and other apparatuses, and can be applied to  
elements which constitute these apparatuses, to a method for  
10 controlling these apparatuses, and to a computer program  
product for providing the control method.

While the present invention has been described with  
respect to what is presently considered to be the preferred  
embodiments, it is to be understood that the invention is  
15 not limited to the disclosed embodiments. To the contrary,  
the present invention is intended to cover various  
modifications and equivalent arrangements included within  
A the spirit and scope of the appended <sup>claims</sup> ~~claimed~~. The scope of  
the following claims is to be accorded the broadest  
20 interpretation so as to encompass all such modifications,  
equivalent structures and functions.